

**PATENT**  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Chia-Hsing Yu, Nai-Shung Chang  
5 Examiner Wang, Albert C.  
Conf. No.: 8325  
Filing Date: 12/05/2001 Art Unit: 2115  
Serial No.: 09/683,243 Docket No.: VIAP0010USA

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AUG 19 2004

**OFFICIAL**

10 Title: INTEGRATED LEVEL SHIFT AND OVERRIDE CIRCUIT FOR A  
PROCESSOR SYSTEM

To: Commissioner for Patents  
P.O. BOX 1450  
15 Alexandria, VA 22313-1450

Subject: Response to the Office Action dated 05/20/2004

Dear Sir:

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**INTRODUCTORY COMMENTS**

In response to the Office action identified above, Claim 2 is amended to put  
claims 2-15 in allowable form.

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